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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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10/666,220

09/19/2003

Hyun-Jin Cho

C002-D-2

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04/01/2005

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EXAMINER

TRAN, MAI HUONG C

ART UNIT

PAPER NUMBER

2818

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PTO 1449 dated 3/18/04 has been received and partially considered except three non patent literature documents. In order for these documents to be considered, please submit a copy of these documents.

- Namati, Farid and Plummer, James, D., A novel High Density, Low Voltage SRAM Cell with a Vertical NDR Device.
- Namati, Farid and Plummer, James, D., A Novel Thyristor-based SRAM Cell for high-speed, low-voltage, giga-scale memories.
- Gribnikov, Z.S., Korobov, V.AI, and Mitin, v.v., The Tunnel Diode as a Thyristor Emitter, Solid-State Electronics.

Mjt

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